



XA-9693A  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshitaka SASAGO et al.

Appln. No.: 10/701,497

Group Art Unit: 2822

Filed: November 6, 2003

Examiner: T. M. THOMAS

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE AND  
MANUFACTURING METHOD THEREOF

Allowed: November 17, 2006

Confirmation No.: 7623

\* \* \*

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450


Attn: Mail Stop Issue Fee

Sir:

Applicants note that the Examiner's Statement of Reasons for Allowance does not acknowledge consideration of the Japan Society of Applied Physics article of record. However, it is apparent that the article must have been considered in view of the Remarks in the Amendment dated October 17, 2006.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

By:   
Mitchell W. Shapiro  
Reg. No. 31,568

MWS:sjk

Miles & Stockbridge P.C.  
1751 Pinnacle Drive, Suite 500  
McLean, Virginia 22102-3833  
(703) 610-8652

February 20, 2007